

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Haruyoshi TOYODA et al.

Application No.: 10/089,192

Filed: March 27, 2002

Docket No.: 112417

For: CAMERA SYSTEM FOR HIGH-SPEED IMAGE PROCESSING

INFORMATION DISCLOSURE STATEMENT

Director of the U.S. Patent and Trademark Office
Washington, D.C. 20231

Sir:

Pursuant to 37 CFR §1.56, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached PTO-1449. Unless otherwise indicated herein, one copy of each reference is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

- ☒ 1. This Information Disclosure Statement is being filed (a) within three months of the U.S. filing date of this non-CPA application, OR (b) before the mailing date of a first Office Action on the merits in the present application. No certification or fee is required.
- ☒ 2. Relevance of the English-language and non-English language references 3-9 are discussed in the present specification.

Respectfully submitted,



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Form PTO-1449 (REV. 8-83)		US Dept. of Commerce PATENT & TRADEMARK OFFICE		ATTY DOCKET NO. 112417	APPLICATION NO. 10/089,192
INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)				APPLICANTS Haruyoshi TOYODA et al.	
				FILING DATE March 27, 2002	
U.S. PATENT DOCUMENTS					
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS
	1	09/807,039 (Atty Dckt: 109147)	04/09/2001	Toyoda et al.	
	2	09/936,267 (Atty Dckt: 110602)	09/10/2001	Shikawa et al.	
FOREIGN PATENT DOCUMENTS					
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS
	3	JP B2 7-62866	07/05/1995	Japan	
	4	JP A 10-145680	05/29/1998	Japan	
	5	WO 95/24793	09/14/1995	Wipo	
	6	JP A 10-21407	01/23/1998	Japan	
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)					
	7	Standley, "An Object Position and Orientation IC with Embedded Imager", Solid State Circuits, Vol. 26, No. 12, pp. 1853-1859, 1991			
	8	Hutchinson et al., "Computing Motion Using Analog and Binary Resistive Networks", Computer, Vol. 21, pp. 52-64, 1988			
	9	Kyuma et al., "Artificial Retinas- fast versatile image processors", Nature, Vol. 372, 1994			
	10	Integrated Vision Products AB", MAPP2200 Product Information Ver 2.1, 1998			
EXAMINER				DATE CONSIDERED	
Examiner: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.					

Date: June 4, 2002